

Microprocessor interfaces

Lecture 10 on Dedicated systems

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seminar by Valentina Di Mauro (PDF)

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 - ASIP design flow
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- example: the Nios-II custom-instruction interface

references

recommended readings:

Schaumont, Ch. 11, Sect. 11.1.1-11.1.5, 11.2.0, 11.3.0-11.3.1, 11.3.3

for further consultation:

Schaumont, Ch. 11, Sect. 11.1.6, 11.2.1-11.2.2, 11.3.2, 11.3.4

Nios II Custom Instruction User Guide, UG-N2CSTNST, 2017.12.22, Intel Corp.